

## Research Assistant

Posted April 11 to April 25, 2024

### Mixed-Signal Circuit Design for Wireline and Security Applications

**NOTE:** This is a 3-month contract. Full or part time status to be determined in collaboration with the selected candidate(s).

#### POSITION SCOPE:

Design and simulation of integrated circuits under the supervision of Professor G. Cowan

#### PRIMARY RESPONSIBILITIES:

- Simulation of mixed-signal circuits using Cadence CAD tools
- Analyze performance using various security or signal-integrity metrics
- Integrated-circuit layout

#### REQUIREMENTS:

- Completed or in-progress university degree in Electrical/Computer Engineering
- Familiarity with transistor-level design of integrated circuits
- Previous experience in integrated circuit layout an asset
- Experience with Cadence tools
- Good spoken and written English language skills
- Strong interpersonal and communications skills.
- Detail-oriented, with strong organizational and problem-solving skills.

**Contract:** 10+ weeks from May 1<sup>st</sup> to August 31<sup>st</sup> 2024

**Hours:** Full or part-time

**Salary:** \$15 - \$20 per hour depending on hours and candidate's qualifications

*Please send your curriculum vitae with a covering letter **by April 25<sup>th</sup>, 2024** to Dr. Glenn Cowan [gcowan@ece.concordia.ca](mailto:gcowan@ece.concordia.ca)*

Concordia University is committed to Employment Equity and encourages applications from women, Aboriginal Peoples, visible minorities, ethnic minorities, and persons with disabilities.